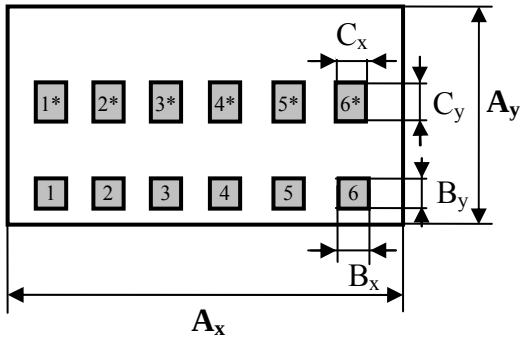
VSP-MIKRON

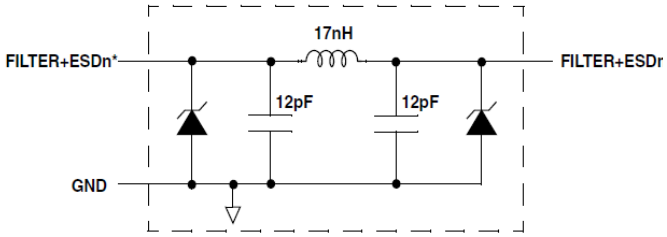
Rev.1. March 2010.

LC-306

PRELIMINARY

6 Channel L-C EMI Filter Array with ESD Protection.





Mechanical date: A_x=1940um, A_y=480um.
B_x=70um, B_y=70um
C_x=82um, C_y=96um

Schematic and pinning diagram.

Chip thickness: 138+/-12um.
Scribe Line width - 60um.
Top Metal: Al – for wire bonding, d=2.2+/-0.2 um.
Back side: Ti-Ni-Ag for soldering.
Back side – GND

Limiting values

Parameter	Symbol	Conditions	Value	Unit
Electrostatic Discharge	V_{ESD}	IEC 61000-4-2, level 4	+/-15 (Contact)	kV
Max. junction temperature	T_J	–	125	ºC

Characteristics (Tj=25ºC)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
L	Channel Inductance	–	–	17	–	nH
C_{TOTAL}	Total Channel Capacitance	V_R=2.5V, 1MHz	18.8	23.5	28.2	pF
I_R	Diode reverse leakage current	V=+3.3V	–	0.1	1.0	µA
V_{SIG}	Signal Clamp Voltage Positive Clamp Negative Clamp	 I_{LOAD}=10 mA I_{LOAD}= -10 mA	 5.6 -1.5	 6.8 -0.8	 9 -0.4	 V V